

SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada)

Seshadri Rao Knowledge Village, Gudlavalleru

M.Tech II Semester (R20/R21) First Mid Term Examinations

TIME TABLE

Timings: 02.00 p.m. to 03.30 p.m.

28th April 2025

Date	Structural Engineering	Power Electronics & Electric Drives	VLSI Design & Embedded Systems	CSE (Artificial Intelligence & Machine Learning) (R21 Regulation)
05.05.2025 (Monday)	Finite Element Methods in Structural Engineering (CE3910)	Switched Mode Power Conversion (EE3911)	VLSI System Design (EC4911)	MEAN Stack Technologies (CS4911)
06.05.2025 (Tuesday)	Structural Dynamics (CE3911)	Power Electronic Control of Electrical Drives (EE3912)	Embedded System Based IoT (EC4912)	Machine Learning (CS4912)
07.05.2025 (Wednesday)	Advanced Design of Steel Structures (CE3912)	Hybrid Electric Vehicles (EE3914)	System on Chip Design (EC4915)	Natural Language Processing (CS4915)
08.05.2025 (Thursday)	Repairs and Retrofitting of Structures (CE3916)	Applications of Power Converters (EE3917)	Communication Buses and Interfaces (EC4917)	Cloud Computing (CS4916)
09.05.2025 (Friday)	English for Research Paper Writing (EG3901)			

Note: Seating arrangement will be notified before the commencement of the examination by the respective departments.


Vice Principal
(Academics)

Copy to: HODs – CE/EEE/ECE/CSE/ENG/CoE

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)